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| Sheet | 1 | of | 1 |

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| | AD | GEERT A.M. VAN DER PLAS, et al.; "A 14-bit Intrinsic Accuracy Q ² Random Walk CMOS DAC"; IEEE Journal of Solid-State Circuits; December 1999; pgs. 1708-1718; Vol. 34, no. 12; IEEE; New York |
| | AE | YONGHUA CONG et al.; "Switching Sequence Optimization for Gradient Error Compensation in Thermometer-Decoded DAC Arrays"; IEEE Transaction on Circuits and Systems – II: Analog and digital Signal Processing; July 2000; pgs. 585-595; Vol. 47, no. 7; IEEE; New York |
| | AF | European Search Report dated September 24, 2003 |
| | | |

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